

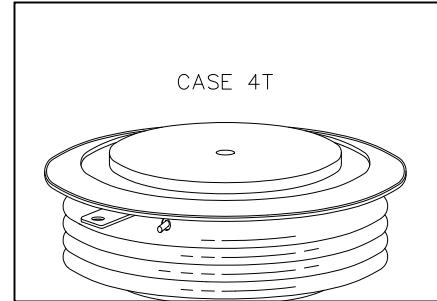
YZPST-N2055MC280

HIGH POWER THYRISTOR FOR PHASE CONTROL APPLICATIONS**Features:**

- . All Diffused Structure
- . Center Amplifying Gate Configuration
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

ELECTRICAL CHARACTERISTICS AND RATINGS**Blocking - Off State**

V_{RRM} (1)	V_{DRM} (1)	V_{RSM} (1)
2800	2800	2900

 V_{RRM} = Repetitive peak reverse voltage V_{DRM} = Repetitive peak off state voltage V_{RSM} = Non repetitive peak reverse voltage (2)

Notes:

All ratings are specified for $T_j=25^\circ\text{C}$ unless otherwise stated.

(1) All voltage ratings are specified for an applied 50Hz/60Hz sinusoidal waveform over the temperature range -40 to $+125^\circ\text{C}$.

(2) 10 msec. max. pulse width

(3) Maximum value for $T_j = 125^\circ\text{C}$.

(4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM} . Gate open. $T_j = 125^\circ\text{C}$.

(5) Non-repetitive value.

Repetitive peak reverse leakage and off state leakage	I_{RRM} / I_{DRM}	10 mA 65 mA (3)
Critical rate of voltage rise	dV/dt (4)	500 V/ μsec

Conducting - on state

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Average value of on-state current	$I_{T(AV)}$		2000		A	$T_c=93^\circ\text{C}$
RMS value of on-state current	I_{TRMS}		2000		A	Nominal value
Peak one cPSTCle surge (non repetitive) current	I_{TSM}		41000 36000		A A	8.3 msec (60Hz), sinusoidal wave- shape, 180° conduction, $T_j = 125^\circ\text{C}$ 10.0 msec (50Hz), sinusoidal wave- shape, 180° conduction, $T_j = 125^\circ\text{C}$
I^2t	I^2t		3.3×10^6		A^2s	8.3 msec and 10.0 msec
Latching current	I_L		800		mA	$V_D = 24\text{ V}; R_L = 12\text{ ohms}$
Holding current	I_H		400		mA	$V_D = 24\text{ V}; I = 2.5\text{ A}$
Peak on-state voltage	V_{TM}		1.45		V	$I_{TM} = 2000\text{ A};$
Critical rate of rise of on-state current (5)	di/dt		200		$\text{A}/\mu\text{s}$	Switching from $V_{DRM} \leq 1000\text{ V}$, non-repetitive

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Gating

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	P _{GM}		200		W	t _p = 40 us
Average gate power dissipation	P _{G(AV)}		5		W	
Peak gate current	I _{GM}		10		A	
Gate current required to trigger all units	I _{GT}		300 150 125		mA	V _D = 6 V; R _L = 3 ohms; T _j = -40 °C
					mA	V _D = 6 V; R _L = 3 ohms; T _j = +25 °C
					mA	V _D = 6 V; R _L = 3 ohms; T _j = +125 °C
Gate voltage required to trigger all units	V _{GT}	0.30	5 3		V	V _D = 6 V; R _L = 3 ohms; T _j = -40 °C
					V	V _D = 6 V; R _L = 3 ohms; T _j = 0-125 °C
					V	V _D = Rated V _{DRM} ; R _L = 1000 ohms; T _j = +125 °C
Peak negative voltage	V _{GRM}		5		V	

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Delay time	t _d		1.5	0.7	μs	I _{TM} = 50 A; V _D = Rated V _{DRM} Gate pulse: V _G = 20 V; R _G = 20 ohms; t _r = 0.1 μs; t _p = 20 μs
Turn-off time (with V _R = -50 V)	t _q		500	250	μs	I _{TM} = 1000 A; di/dt = 25 A/μs; V _R ≥ -50 V; Re-applied dV/dt = 20 V/μs linear to 80% V _{DRM} ; V _G = 0; T _j = 125 °C; Duty cPSTClе ≥ 0.01%
Reverse recovery charge	Q _{rr}		*		μC	I _{TM} = 1000 A; di/dt = 25 A/μs; V _R ≥ -50 V

* For guaranteed max. value, contact factory.

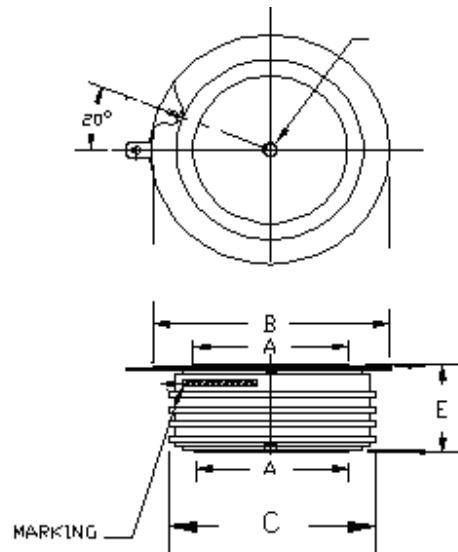
THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T _j	-40	+125		°C	
Storage temperature	T _{stg}	-40	+150		°C	
Thermal resistance - junction to case	R _{Θ(j-c)}		0.025 0.050		°C/W	Double sided cooled Single sided cooled
Thermal resistamce - case to sink	R _{Θ(c-s)}		0.010 0.020		°C/W	Double sided cooled * Single sided cooled *
Mounting force	P	20	24		kN	

* Mounting surfaces smooth, flat and greased

Note : for case outline and dimensions, see case outline drawing in page 4 of this Technical Data

YZPST-N2055MC280



A: 47 mm
B: 74 mm
C: 66 mm
E: 26 mm