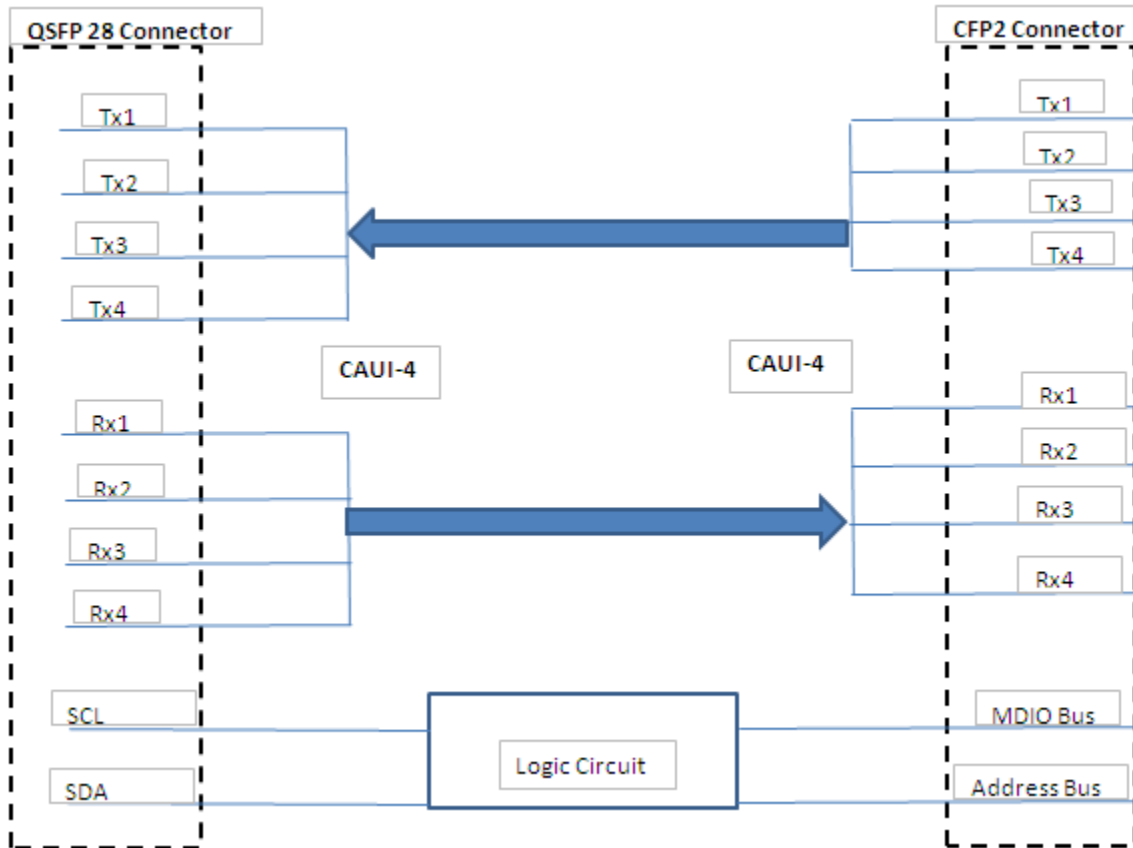


100G CFP2 TO QSFP28 Adaptor Module

GENERAL DESCRIPTION

General Information	
Manufacturer Part Number	UJCRVCP2-Q1H
Product Name	UJCRVCP2-Q1H Adaptor Module
Product Type	Adaptor Module
Power consumption	Max: 0.25W (3.3V voltage, max current 0.08A)
Features	<ul style="list-style-type: none"> • CAUI -4 Electrical Interface:4 Lanes @25.78Gbit/s for CFP2 port • CAUI -4 Electrical Interface:4 Lanes @25.78Gbit/s for QSFP28 port • Hot Pluggable • MDIO, I2C Support • Compliant to CFP2 MSA • RoHS-6 compliant • Case operating temperature: 0 to 70 °C • The UJCRVCP2-Q1H Adaptor Module converts a 100 Gigabit CFP2 port into a 100 Gigabit QSFP28 port. With the Adaptor Module, customers have the flexibility to use the 100 Gigabit CFP2 interface port of a switch with CFP2 modules or QSFP28 modules.
I/O Expansions	
CFP2 port	MDIO
QSFP28 port	I2C
Physical Characteristics	
Dimensions	12.4mmHeight x 41.5Width x 108.2 Depth

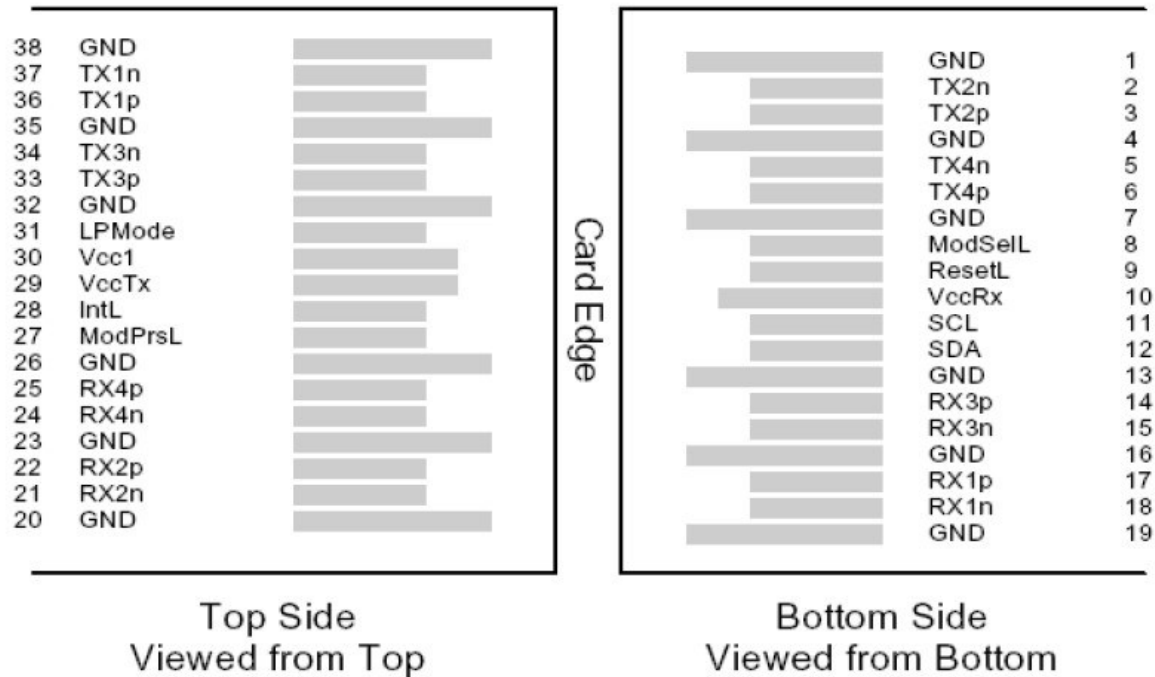
Block Diagram



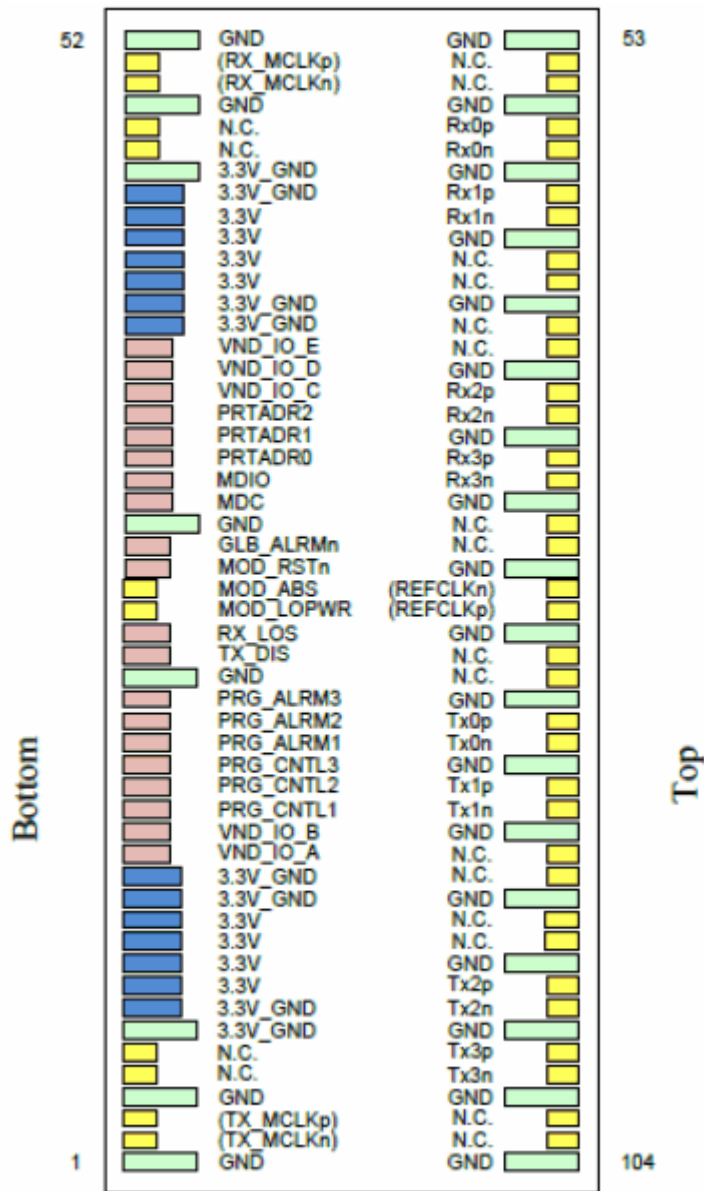
CAUI-4 Interface (QSFP28 port&CFP2 port)

CAUI Receiver Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Signal Rate Per Lane			25.78		Gb/s	
Differential data output swing per lane	$V_{out,pp}$	CAUI-4 as defined by IEEE P802.3bm			mV	
Data output rise time	t_r				ps	
Data output fall time	t_f				ps	
CAUI Transmitter Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Signal Rate Per Lane			25.78		Gb/s	
Frequency offset		-100		100	ppm	100G Ethernet
		-20		20	ppm	OTU4
Input differential impedance	R_{in}	CAUI-4 as defined by IEEE P802.3bm			Ω	
Differential data input swing per lane	$V_{in,pp}$				mV	
Data input rise time tolerance	t_r				ps	
Data input fall time tolerance	t_f				ps	

PIN Assignment of QSFP28 port



PIN Assignment of CFP2 port



Package Outline (Unit:mm)

Units in mm

